



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: PATEL, et al.

Attorney Docket No.: P19-US

Serial No.: -- 10/005,308

Group Art Unit: 2812

Filed: -- December 03, 2001

Examiner: -- Unknown

For: A METHOD FOR FORMING A MICRO-ELECTROMECHANICAL DEVICE

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INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Assistant Commissioner of Patents
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February 14, 2003

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO/SSSB/08A. Copies of the cited references are enclosed.

No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the last of (1) issuance of a first official action on the merits and (2) expiration of the three month period following filing of the above-captioned application.

The above information is presented so that the Patent and Trademark Office can determine any materiality thereof to the claimed invention. It is respectfully requested that the information be considered during the prosecution of this application and that the cited documents be listed on the front page of any patent issuing from this application.

The Patent Office is authorized to charge our Deposit Account No. 501516 for

any fee which it deems to be required to effect consideration of this statement.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Gregory R. Muir", written over a horizontal line.

Gregory R. Muir
Attorney for Applicant(s)
Registration No. 35,293

Attorney Docket No. P19-US
REFLECTIVITY, INC.
350 Potrero Avenue
Sunnyvale, CA 94085

Tel: 408) 737-8100 x. 136
Fax: 408-737-8153
GRM:lb



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			Application Number	10/005,308	
			Filing Date	December 3, 2001	
			First Named Inventor	PATEL et al.	
			Art Unit	2812	
			Examiner Name		
Sheet	1	of	3	Attorney Docket Number	P19-US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. 1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code* (if known)			
	AA	US- 4,178,077	12-11-1979	TE VELDE	
	AB	US- 4,309,242	12-05-1982	TE VELDE	
	AC	US- 5,293,511	03-08-1994	PORADISH et al.	
	AD	US- 5,527,744	06-18-1996	MIGNARDI et al.	
	AE	US- 5,872,046	02-16-1999	KAERIYAMA et al.	
	AF	US- 5,963,289	10-05-1999	STEFANOV et al.	
	AG	US- 6,165,885	12-26-2000	GAYNES et al.	
	AH	US- 6,207,548 B1	03-27-2001	AKRAM et al.	
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	AK	US- 6,287,940 B1	09-11-2001	COLE et al.	
	AL	US- 6,303,986 B1	10-16-2001	SHOOK	
	AM	US- 6,323,550 B1	11-27-2001	MARTIN et al.	
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	AP	US- 2001/0022207 A1	09-20-2001	HAYS et al.	
	AQ	US- 2001/0034076 A1	10-25-2001	MARTIN	
		US-			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. 1	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
	BA	EP-1097901-A2	05-09-2001	HA et al.		
	BB	EP-1101730-A2	05-23-2001	WOOD et al.		
	BC	EP-1167281-A2	01-02-2002	KANG et al.		
	BD	WO-01/10718-A1	02-15-2001	GUANN-PYNG et al.		
	BE	WO-01/20671-A1	03-22-2001	XU et al.		
	BF	WO-02/12116-A2	02-14-2002	BROSNIHAN et al.		
	BG	WO-02/12116-A3	02-14-2002	BROSNIHAN et al.		
	BH	JP-2001129800-A	05-15-2001	MASAKI		
	BI	JP-2001144117-A	05-25-2001	OAKATTO et al.		
	BJ	JP-2001196484-A	07-19-2001	JU et al.		

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			Group Art Unit	2812	
			Examiner Name		
Sheet	2	of	2	Attorney Docket Number	P19-US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials²	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T²
	CA	ESPINOSA et al., "Identification of Residual Stress State in an RF-MEMS Device", MTS Systems Corporation white paper (May 2000).	
	CB	FRANKA et al., "Solder Bump Technology: Present and Future", Semiconductor Fabtech (May 1995).	
	CC	GLENN et al., "Packaging Microscopic Machines", Machine Design (Dec. 7, 2000).	
	CD	HARSH et al., "Flip-Chip Assembly for Si-Based MEMS", Proceedings of the 1999 IEEE International Conference on Microelectromechanical Systems (MEMS '99), Orlando, FL (Jan. 17-21, 1999), pp. 273-278.	
	CE	IRWIN et al., "Quick Prototyping of Flip Chip Assembly with MEMS", University of Colorado at Boulder white paper (July 17, 2000).	
	CF	IRWIN et al., "Quick Prototyping of Flip-Chip Assembly with MEMS", portions of slide presentation from the NSF Center for Advanced Manufacturing and Packaging of Microwave, Optical and Digital Electronics at the University of Colorado at Boulder (1998).	
	CG	LEE et al., "High-Q Tunable Capacitors and Multi-way Switches Using MEMS for Millimeter-Wave Applications", portions of slide presentation from the NSF Center for Advanced Manufacturing and Packaging of Microwave, Optical and Digital Electronics at the University of Colorado at Boulder (Sept. 1998).	
	CH	LEE et al., "Use of Foundry Services to Prototype MEMS for Millimeter-wave Applications", portions of slide presentation from the NSF Center for Advanced Manufacturing and Packaging of Microwave, Optical and Digital Electronics at the University of Colorado at Boulder (1998).	
	CI	LUXBACHER, T., "Spray Coating for MEMS, Interconnect & Advanced Packaging Applications", HDI Magazine (May 1999) (abstract only).	
	CJ	MOORE, D., "Automation Requirements for Die Bonding Process", Electronics Engineer (July 2000).	
	CK	TSAU, C., "Wafer-Level Packaging", MIT Microsystems Technology Laboratories Annual Report (May 2000), p. 49.	

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SB08A - Information Disclosure Statement by Applicant - Attorney Docekt No. P19-US - 4 pages

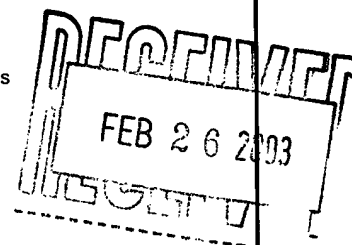
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